

<b>Notice of References Cited</b>	Application/Control No. 09/583,386	Applicant(s)/Patent Under Reexamination CARLEY, L. RICHARD	
	Examiner Erik Kielin	Art Unit 2813	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,573,679	11-1996	Mitchell et al.	216/2
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	C	US-5,834,333	11-1998	Seefeldt et al.	438/409
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	E	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

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	U	Wolf, et al. Silicon Processing for the VLSI Era, Vol. 1-Process Technology, Lattice Press: Sunset Beach CA, 1986, pp. 428-429.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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